



SFP SERIES

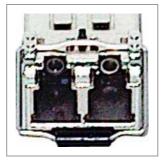
SMALL FROM FACTOR PLUGGABLE

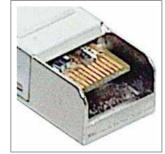
850nm 1.25Gbps Multi Mode SFP **LCS-MGBIC-SX**



850 nm 1.25Gbps Multi Mode SFP LC Transceiver Module 3.3V







850nm 1.25Gbps Multi Mode SFP

Features

- . Small From Factor Pluggable MSA compliant.
- . For Multi Mode Applications.
- . Standard LC Duplex Connector.
- . Up to 850 nm & 1.25 Gb/s .
- . Compliant for IEEE-802.3z Gigabit Ethernet.
- . Power supply: +3.3V.
- . EEPROM with serial ID functionality.
- . TTL Signal detect indicator.
- . PECL input & output logic levels.
- . Uncooled VCSEL structure laser.
- . $0 \square \sim +70 \square$ operating temperature.
- . Class 1M laser safety compliance.
- . 2²³ –1 PRBS, BER=1*10⁻¹⁰.
- . 550 m reach

Specifications

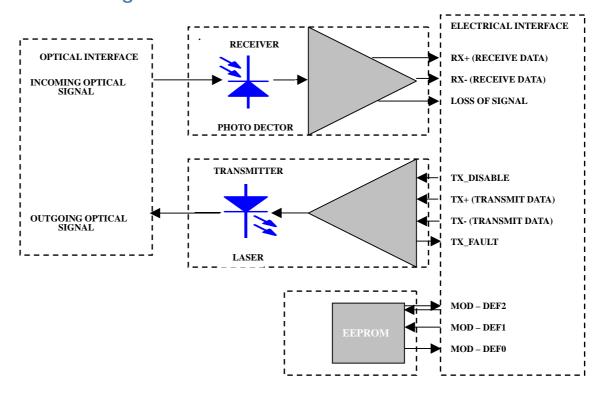
Absolute Maximum Ratings						
Parameter	Symbol	Min	Max	Unit		
Operating temperature	T_{opr}	0	+70	°C		
Storage temperature	T _{stg}	-40	+85	°C		
Lead soldering limits	-	-	260/10	°C/sec		
Supply voltage	VccT	-0.5	4	V		

Electrical Characteristics					
Parameter	Symbol	Min	Typical	Max	Unit
Transmitter:					
Data rate (NRZ)	В	-	1250	-	Mb/s
Data DECL Differential input (6)	V _{il}		-	1.85	V
Data PECL Differential input (6)	V _{ih}	2.15			v
Supply voltage	VCCT	3.1	3.3	3.5	V
Supply current	ICCT	-	130	-	mA
Receiver:					
Data rate (NRZ)	В	-	1250	-	Mb/s
Output rise time (10-90%)	tr	-	-	400	ps
Output fall time (10-90%)	tf	-	-	400	ps
Data PECL output (6)	V _{OL}	-	-	1.65	V
Data PECE output (6)	V _{OH}	2.25	-	-	V
Supply voltage	VCCR	3.1	3.3	3.5	V
Supply current	ICCR	-	120	-	mA
Hysteresis		-	2.5	-	dB

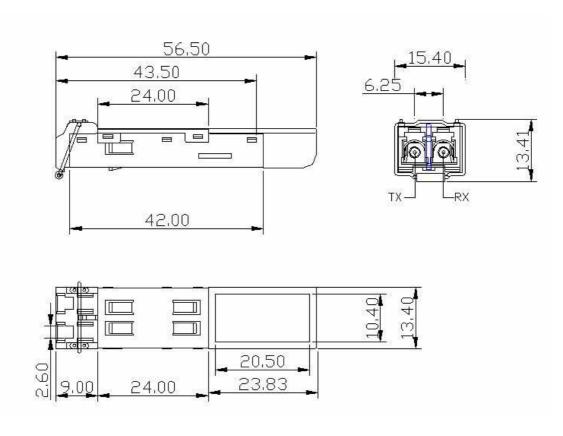
Optical Characteristics						
Paramete	Symbol	Min	Typical	Max	Unit	
Transmitter:						
Optical output (avg.) (1) (3)	Po	-9.5	-	-4	dBm
Extinction ratio		ER	10	-	-	dB
Output rise time (10-9	0%)	tr	-	-	400	ps
Output fall time (10-90)%)	tf	-	-	400	ps
Optical wavelength		λ	830	850	860	nm
Spectral width		Δλ	-	0.85	-	nm
Receiver:				•	•	•
Optical input (avg.)	Б		00			-ID
sensitivity (1) (5)	P _{IN}	-	-20	-	-	dBm
Saturation		-	-	-	-3	dBm
Optical wavelength		λ	770	-	860	nm
Signal detect asserted	l (avg)	PA	-	-	-20	dBm
Signal detect deasser	ted (avg)	P _D	-31	-	-	dBm
Signal Detect-Hystere	sis	P _A -P _D	1.0	-	-	dB
Signal Detect Assert T	īme	T _{SD+}	-	-	100	μs
Signal Detect Deassert Time		T _{SD-}	-	-	100	μs
Differential Output Voltage		V_{DEF}	0.37	-	2.0	V
Receiver Loss of Signa Voltage-low	RX_LOS _L	0	-	0.35	V	
Receiver Loss of Signa Voltage-High	RX_LOS _H	2.4	-	Vcc	V	
Receiver Loss of Signa Time (off to on)	I _{ARX_LOS}	-	-	100	μs	
Receiver Loss of Signa Time (on to off)	I _{DRX_LOS}	-	-	100	μs	

Note	
1	With 0.275 NA, 62.5/125μm Fiber.
2	Driven with a differential signal
3	Class 1M eye safe per FDA and IEC.
4	Compliant with IEEE 802.3Z Gigabit Ethernet.
5	2 ²³ - 1 PRBS, BER= 1*10 ⁻¹⁰ .
6	PECL Differential Voltage Mode.
7	Take normal ESD precautions when handing this product.

Function Diagram

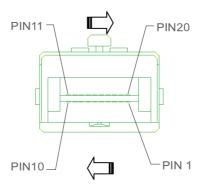


Dimensions & Electrical Pin configuration



SFP to host connector Pin Assignment

AS the LCS-MGBIC-SX is inserted, first contact is made by the housing ground shield, Discharging any potentially component-damaging static electricity. Ground pins engage next and are followed by Tx and Rx power supplies. Finally, signal lines are connected. Pin functions and sequencing are listed in next Table



PINOL	PINOUT TABLE					
Pin	Symbol	Functional Description				
1	T _{GND}	Transmit Ground				
2	TX_FAULT	Transmit Fault				
3	TX_DISABLE	Transmit Disable				
4	MOD_DEF(2)	SDA Serial Data Signal				
5	MOD_DEF(1)	SCL Serial Clock Signal				
6	MOD_DEF(0)	TTL Low				
7	RATE SELECT	Open Circuit				
8	RX_LOS	Receiver Loss of Signal, TTL High, open collector				
9	R _{GND}	Transmitter Ground				
10	R _{GND}	Transmitter Ground				
11	R _{GND}	Receiver Ground				
12	RX-	Receiver Data Bar, Differential PECL, ac coupled				
13	RX+	Receiver Data, Differential PECL, ac coupled				
14	R _{GND}	Receiver Ground				
15	V _{CCR}	Receiver Power Supply				
16	V _{CCT}	Transmitter Power Supply				
!&	T _{GND}	Transmitter Ground				
18	TX+	Transmit Data, Differential PCEL, ac coupled				
19	TX+	Transmit Data, Differential PCEL, ac coupled				
20	T _{GND}	Transmitter Ground				

Tx Disable Characteristics

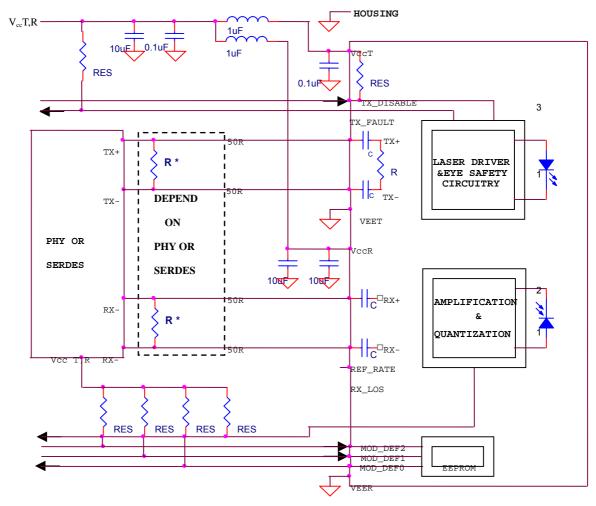
Input Level (LV-TTL)	Tx Function	
Low	ON	
High	OFF	
Non-Connect	ON	

(Disable Pin Truth Table)

SFP Serial ID

DATA	LENGTH	NAME OF	DATA TO BE INCLUDE IN THE FIELD FOR SW		
ADDRESS	(BYTES)	FIELD			
	T	Base ID Fields	T		
0	1	Identifier	Type of serial transceiver (03h=Transceiver)		
1	1	Reserved			
2	1	Connector	Code of optical connector type(07h=LC)		
3-10	8	Transceiver	00000012040C00h=1000BASE-SX (Gigabit Ethernet		
			compliance code for optical compatibility		
11	1	Encoding	03h=NRZ Encoding		
12	1	BR. Nominal	Nominal baud rate, units of 100MHz(0C=1.25Gbps)		
13	1	Reserved			
14	1	Length(9µ)-km	Link length supported for 9/125 μm fiber, units of km (00h)		
15	1	Length(9µ)	Link length supported for 9/125 μm fiber, units of 100m(00h)		
16	1	Length(50µ)	Link length supported for 50/125 μm fiber, units of		
			10m(37h=550m)		
17	1	Length(60µ)	Link length supported for 60/125 μm fiber, units of		
			10m(1Bh=270m)		
18	1	Length(Copper	Link length supported for copper, units of 10m(00h)		
)			
19	1	Reserved			
20-35	16	Vendor name	SFP vendor name:434F4E41532020202020202020202020		
36-39	4	Vendor OUI	SFP transceiver vendor IEEE company ID		
40-55	16	Vendor PN	Part Number		
56-59	4	Vendor rev	Revision level for part number		
60-62	3	Reserved			
63	1	CC_BASE	Check code for Base ID Fields(0 – 62)		
		EXTENDED	ID FIELDS		
64	1	Reserved			
65	1	Options	Indicates which optional SFP signals are implemented		
			(1Ah=RX_LOSS,TX_FAULT,TX_DISABLE all supported		
66	1	BR. max	Upper baud rate margin, units of %(00h)		
67	1	BR. min	Lower baud rate margin, units of %(00h)		
68-83	16	Vendor SN	Serial number (ASCII)		
84-91	8	Date code	Manufacturing date code		
92-94	3	Reserved			
95	1	CC_EXT	Check code for Extended ID Fields(64 – 94)		
	VENDOR SPECIFIC ID FIELDS				
96-127	32	Readable	Vendor specific data, read only		

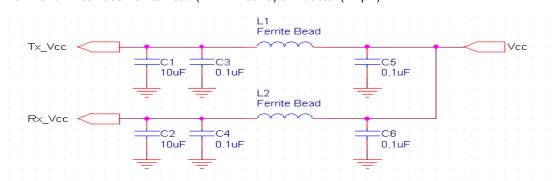
Typical application Circuit



*NOTE 4.7K<RES<10K

Power Coupling

The L1 and L2 can use Ferrite Bead (BLM11A601S) or inductor (4.7 μ H)



L1/2 = 1 uH

Value of R5/6/9/10/11/12 may vary as long as proper 50Ω termination or 100Ω differential Is Provided. For good EMI performance, the power supply filter is required. Use short tracks from the inductor

L1/2 to the module VccTx/VccRx.

Qualification Information

HEADING	TEST	CONDITIONS	SAMPLE SIZE	REFERENCE
	Mechanical Shock	5 times/axis	44	MIL-STD-883
		500G , 1.0ms	11	Method 2002
	Vibration	20G , 20Hz - 2000HZ	11	MIL-STD-883
Mechanical		4min/cycle ,4cycles/axis	11	Method 2007
&	Thermal Shock	Delta T=100	11	MIL-STD-883
Physical			11	Method 2003
i nyolodi	Solderability	_	11	MIL-STD-883
				Method 2007
	Fiber Pull	1Kg; 3times; 5sec	11	Bellcore 983
	Accelerated Aging	85 , 5000hrs	0.5	Bellcore 983
			25	Section 5.18
	High Temperature	85 , 2000hrs	11	Bellcore 983
	Storage		11	
	Low Temperature	-40 , 2000hrs	11	Bellcore 983
Endurance	Storage		11	
Endurano	Temperature	500 cycles.	11	Bellcore 983
	Cycling			Section 5.20
	Cyclic Moisture	10 cycles	11	Bellcore 983
	Resistance			Section 5.23
	Damp Heat	40 , 95%	11	MIL-STD-202
		RH , 1344hrs		Method 103
	Internal Moisture	<5000ppm water vapor	11	MIL-STD-883
				Method 1018
Special	Flammability			TR357
Test				Sec.4.4.2.5
	ESD Threshold	-	6	Bellcore 983
				Section 5.22



